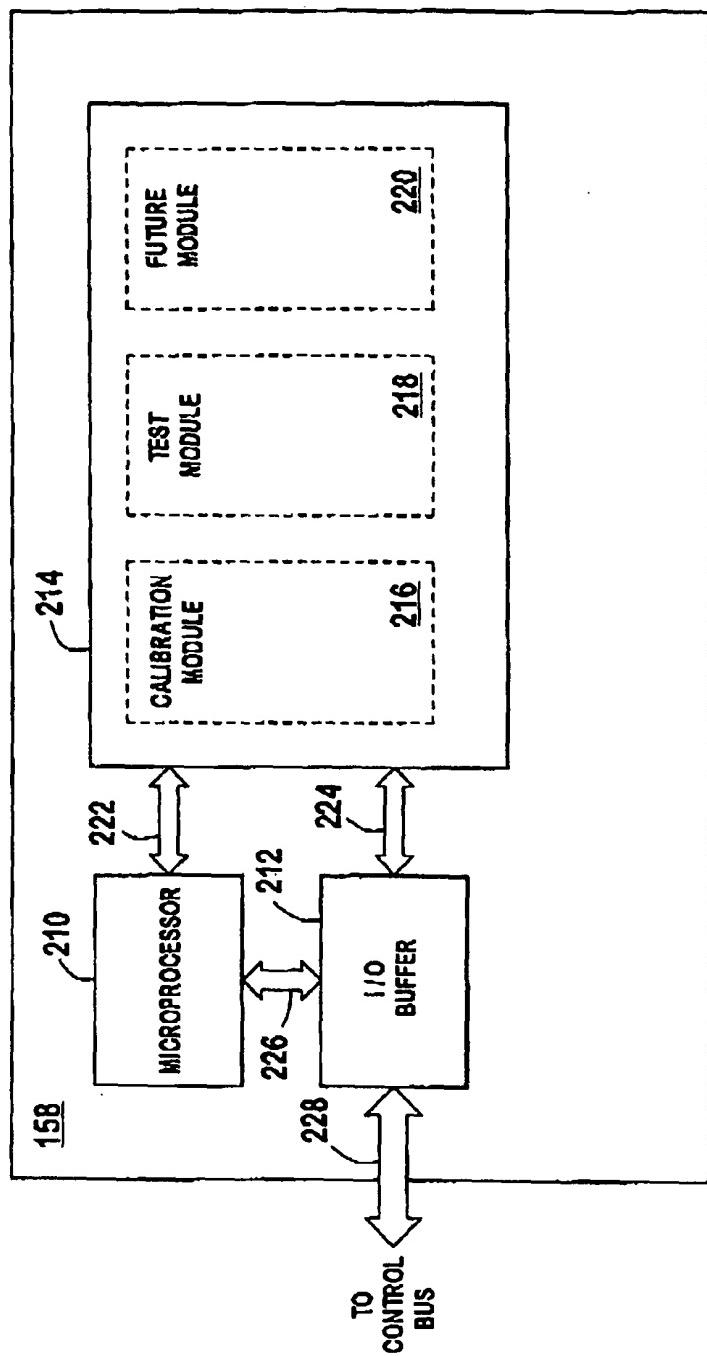


FIG. 1

104

FIG. 2



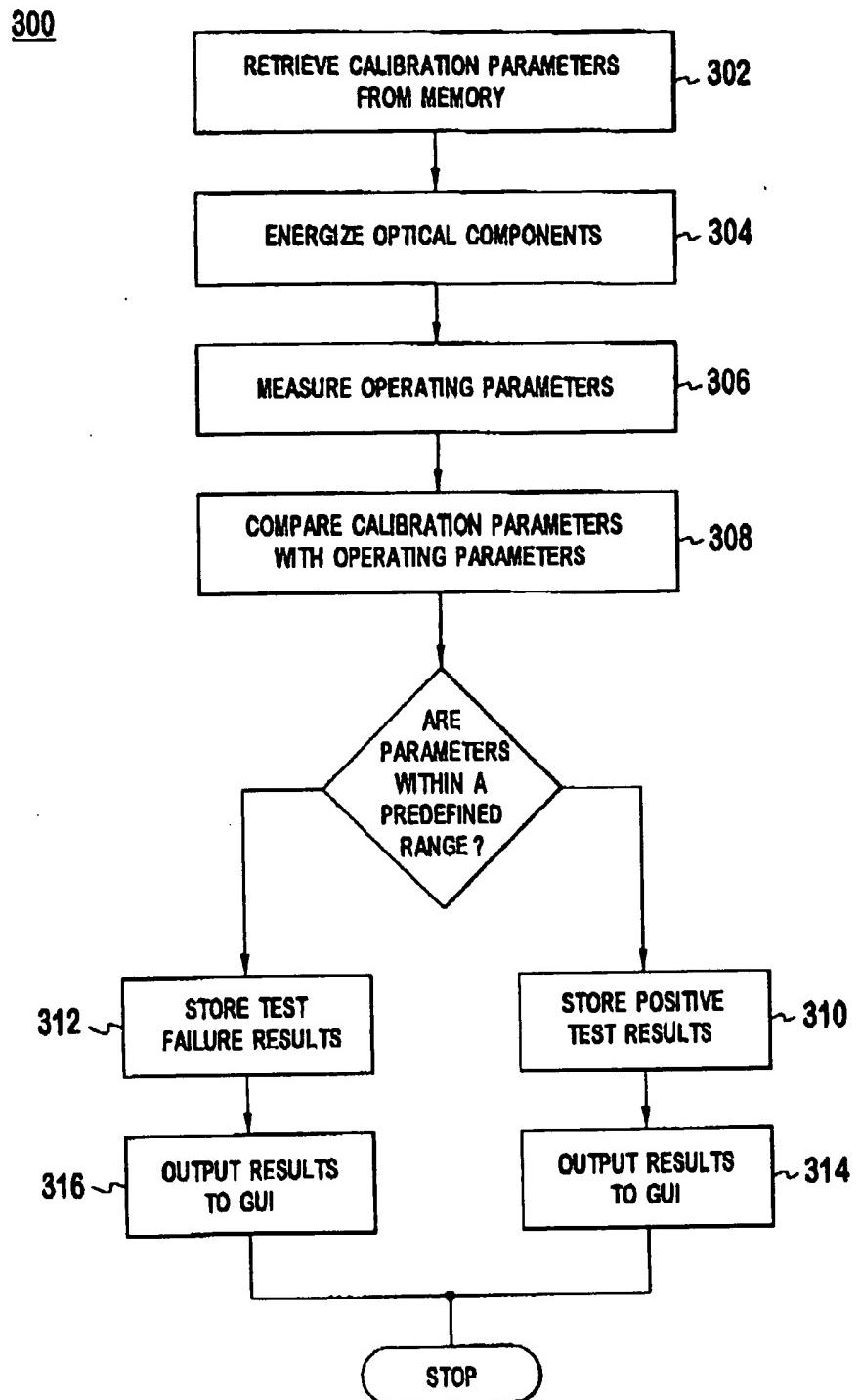


FIG. 5

400

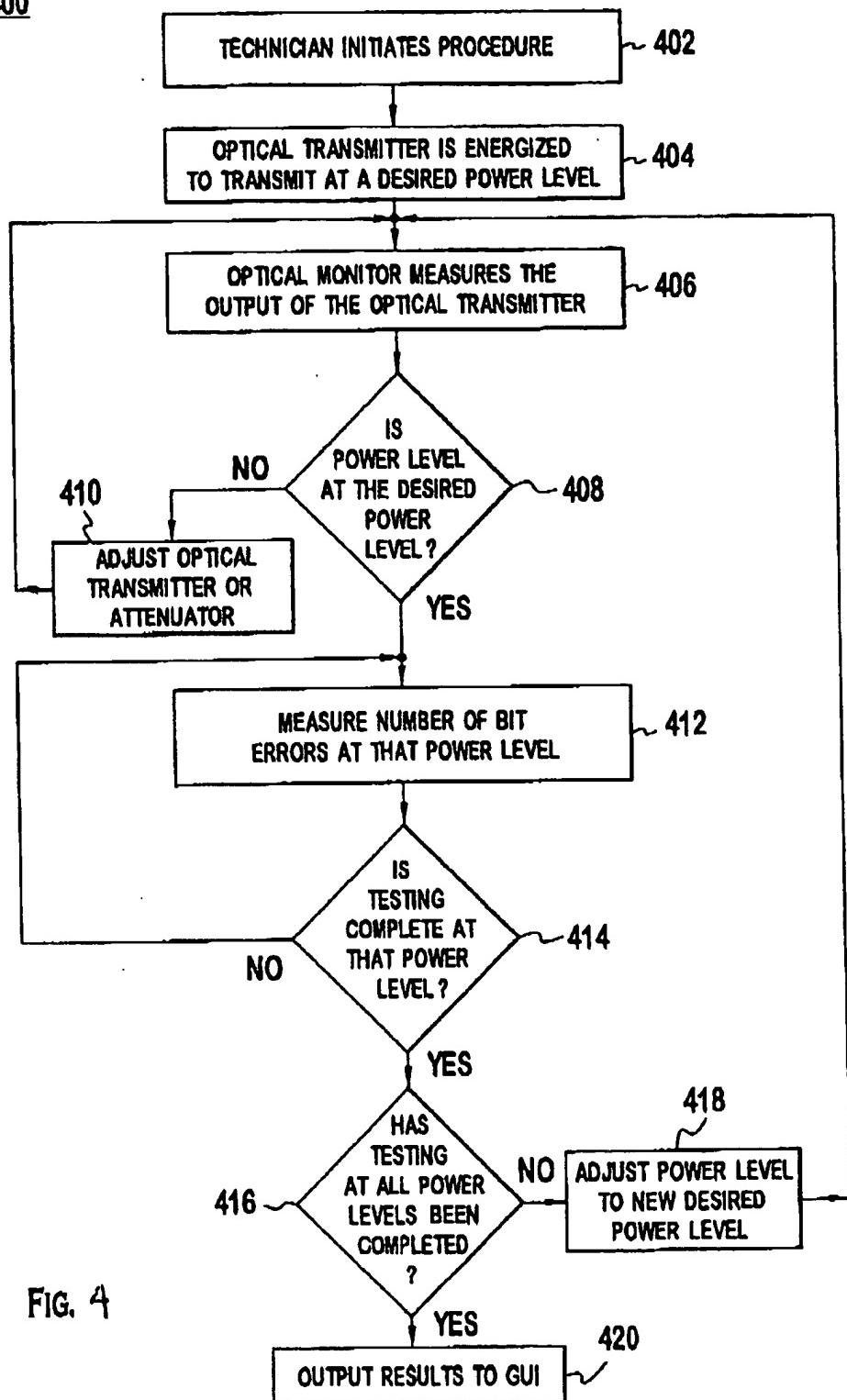


FIG. 4

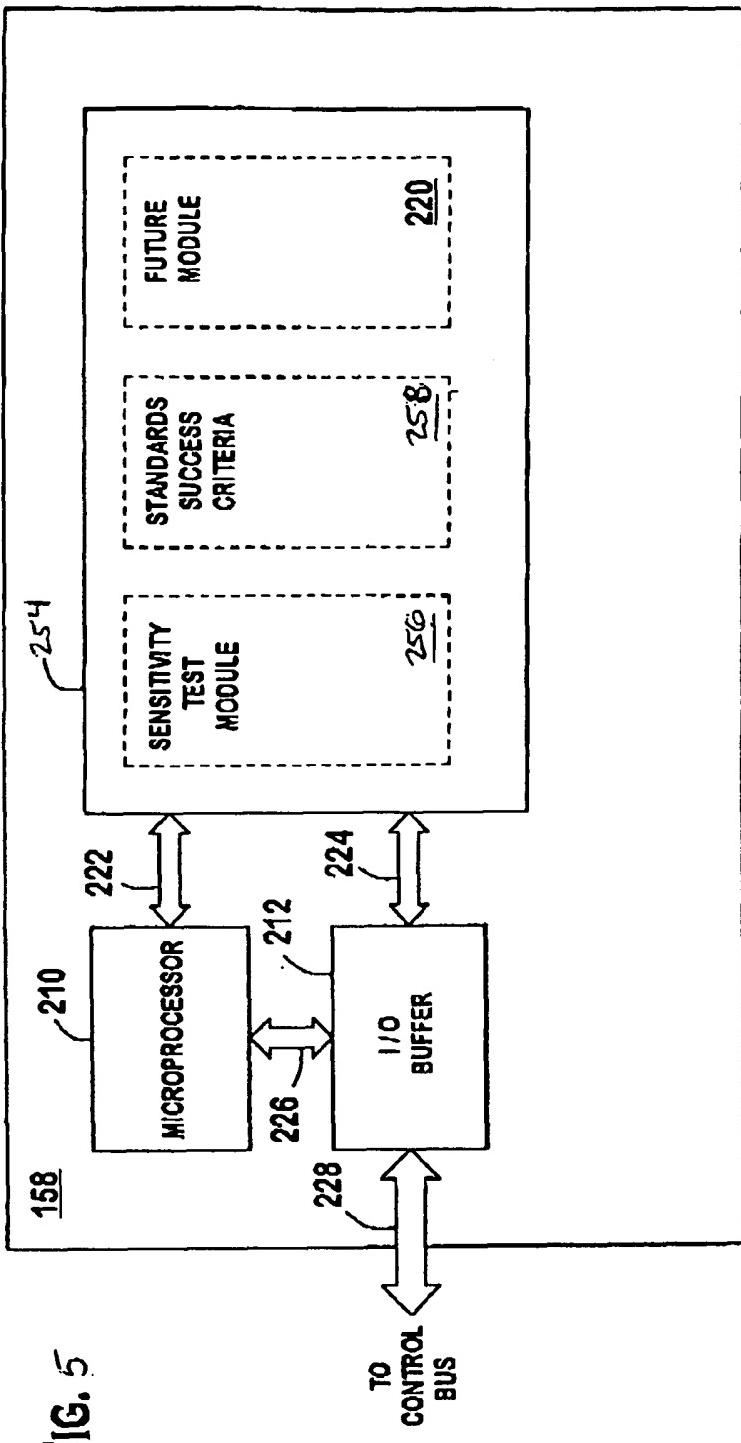


FIG. 5

FIG. 6

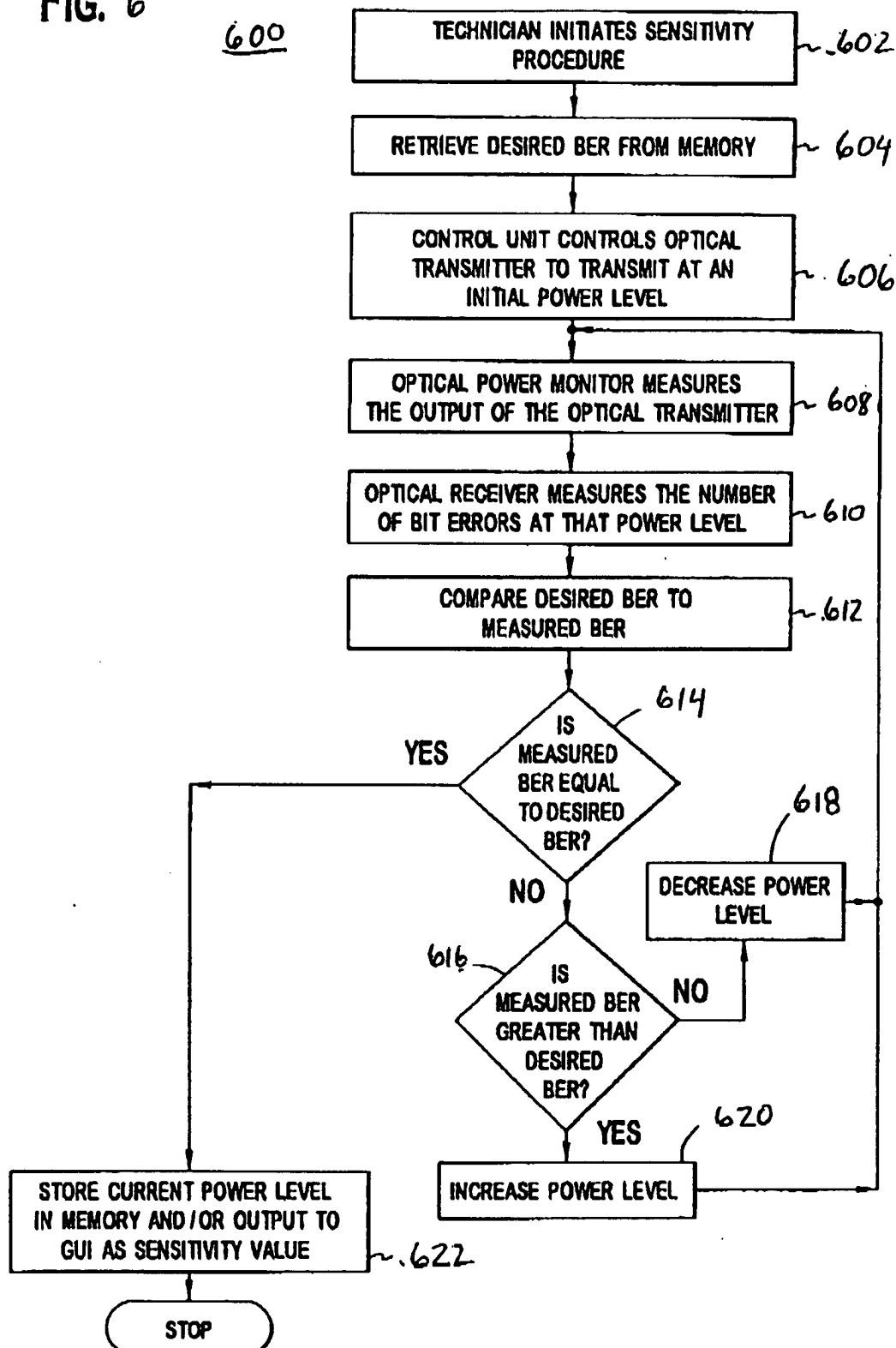
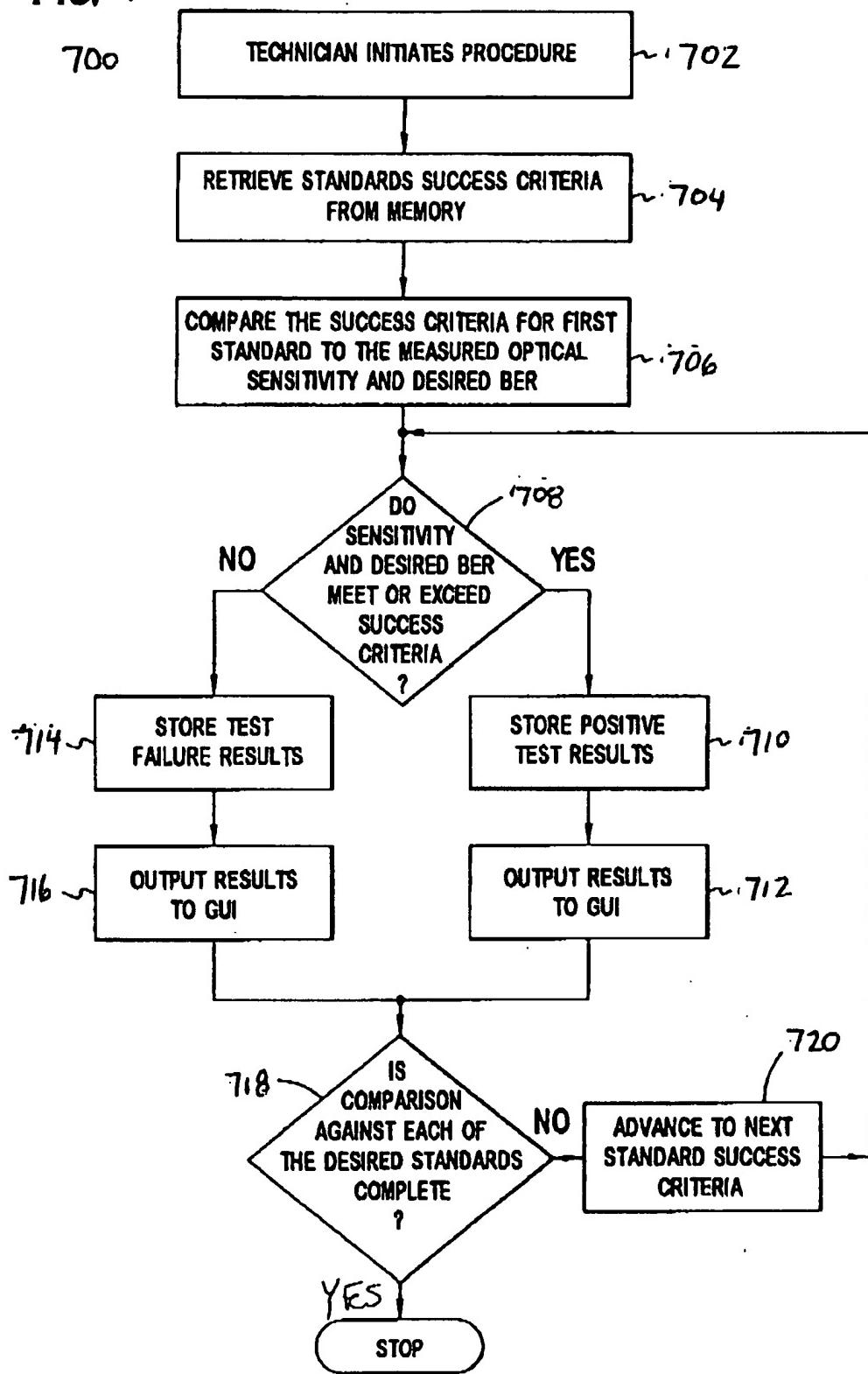


FIG. 7



160

TEST IN PROGRESS

TRANSMITTER POWER \_\_\_\_\_

RECEIVER POWER \_\_\_\_\_

BIT ERROR RATE \_\_\_\_\_

130

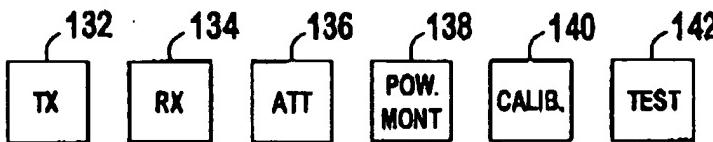


FIG. 8